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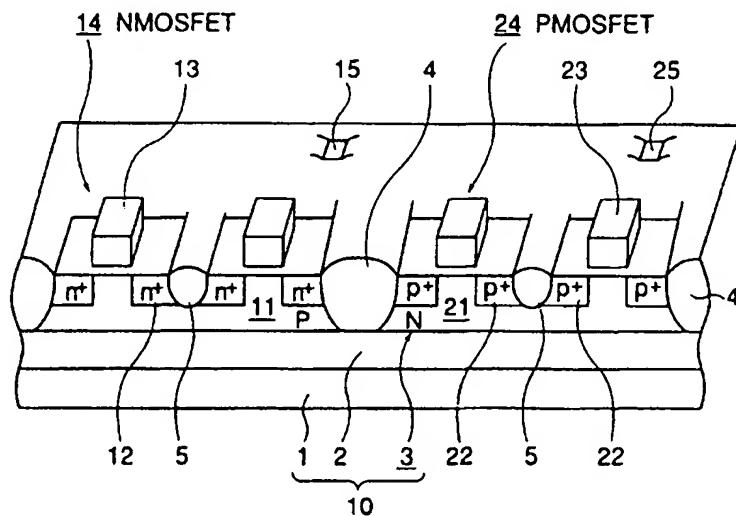
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(54) SOI semiconductor device and method for manufacturing the same

(57) A SOI semiconductor device comprises: a SOI substrate in which a buried dielectric film and a surface semiconductor layer are laminated; at least one well formed in the surface semiconductor layer; and at least one transistor which is formed in the well and has a channel region and source/drain regions in the surface semiconductor layer, wherein the well is completely iso-

lated in the surface semiconductor layer and has a well-contact for applying a bias voltage to the well, the transistor is isolated by a device isolation film formed in a surface of the surface semiconductor layer, the channel region is partially depleted, and the surface semiconductor layer under the source/drain regions is fully depleted.

Fig. 1





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Application Number
EP 99 30 3815

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BERLIN	17 April 2000	Juhl, A	
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